In the Claims:

This listing of claims replaces all prior versions and listing of claims in the application.

Claims 1-20 (Canceled).

21. (Currently amended) A method of converting data between an unencrypted format and an encrypted format, the data being organized in bit words and being stored in at least one register, the method comprising:

using a circuit cooperating with the at least one register to convert ~~converting~~ the data by at least performing a plurality of transformation rounds, each transformation round having a respective round key and comprising

applying at least one transformation to a two-dimensional array of rows and columns of bit words defining a state array,

exchanging each of the rows with a respective column of the state array to form a transposed state array for at least one of the transformation rounds so that the at least one transformation is applied to the transposed state array,

transposing the respective round key, and

applying the respective transposed round key to the state array in at least one of the transformation rounds; and

using the circuit to transpose ~~transposing~~ an output of a final round from the plurality of transformation rounds.

22. (Previously presented) A method according to Claim 21 wherein the bit words are 8-bit words.

23. (Previously presented) A method according to Claim 21 wherein the state array is a 4 x 4 matrix of bit words.

24. (Previously presented) A method according to Claim 21 wherein the plurality of transformation rounds comprises at least 10 transformation rounds.

25. (Previously presented) A method according to Claim 21 wherein performing further comprises performing at least one transformation round on a non-transposed state array.

26. (Canceled).

27. (Canceled).

28. (Previously presented) A method according to Claim 21 further comprising adding code to transpose the respective round key for each of the plurality of transformation rounds.

29. (Previously presented) A method according to Claim 21 wherein each respective round key is applied according to a round key schedule.

30. (Previously presented) A method according to Claim 29 wherein the round key schedule comprises a transposed round key schedule.

31. (Previously presented) A device for converting data between an unencrypted format and an encrypted format, the device comprising:

at least one register configured to store the data in the form of bit words; and

a circuit configured to convert the data by at least

performing a plurality of transformation rounds, each transformation round having a respective round key and comprising

applying at least one transformation to a two-dimensional array of rows and columns of bit words defining a state array,

exchanging each of the rows with a respective column of the state array to form a transposed state array for at least one of the transformation rounds so that at least one transformation is applied to the transposed state array,

transposing the respective round key, and

applying the respective transposed round key to the state array in at least one of the transformation rounds, and

transposing an output of a final round from the plurality of transformation rounds.

32. (Previously presented) A device according to Claim 31 wherein said at least one register is configured to store bit words as 8-bit words.

33. (Previously presented) A device according to Claim 31 wherein said circuit is configured to operate on a state array comprising a 4x4 matrix of bit words.

34. (Previously presented) A device according to Claim 31 wherein said circuit is configured to perform a plurality of transformation rounds performs at least 10 transformation rounds.

35. (Previously presented) A device according to Claim 31 wherein said circuit comprises at least one S-box processing module, said at least one S-box processing module being configured to operate on a group of bit words defining a cell of a column of the state array.

36. (Previously presented) A device according to Claim 35 wherein the at least one S-box processing module comprises a plurality of S-box modules, each of the plurality of S-box modules being configured to operate on a corresponding cell of a column of the state array.

37. (Previously presented) A device according to Claim 36 wherein the column of the state array comprises four cells.

38. (Previously presented) A device according to Claim 31 wherein the circuit further comprises a plurality of shift column modules, each of said plurality of shift column modules being configured to perform a column shift operation on a column of the state array.

39. (Previously presented) A device according to Claim 38 wherein a column shift operation performed by each of said plurality of shift column modules generates shift column data, and wherein said circuit further comprises a single mix column module to perform column mix operations on shift column data.

40. (Previously presented) A device according to Claim 31 wherein said circuit is an encoder for converting data from an unencrypted data format to an encrypted data format.

41. (Previously presented) A device according to Claim 40 wherein said circuit is an embedded system for use in a smart card.

42. (Previously presented) A device according to Claim 31 wherein said circuit is a decoder for converting data from an encrypted data format to an unencrypted data format.

43. (Previously presented) A device according to Claim 42 wherein said circuit is an embedded system for use in a smart card.

44. (Canceled).

45. (Canceled).

46. (Canceled).

47. (Canceled).

48. (Currently amended) A method of converting data between an unencrypted format and an encrypted format, the data being organized in 8-bit words and being stored in at least one register, the method comprising:

using a circuit cooperating with the at least one register to convert ~~converting~~ the data by at least performing a plurality of transformation rounds for converting the data, each transformation round having a respective round key and comprising

applying at least one transformation to a two-dimensional array of rows and columns of 8-bit words defining a state array comprising a 4 x 4 matrix of 8-bit words,

exchanging each of the rows with a respective column of the state array to form a transposed state array for at least one of the transformation rounds so that the at least one transformation is applied to the transposed state array,

transposing the respective round key, and

applying the respective transposed round key

to the state array in at least one of the transformation rounds; and

using the circuit to transpose ~~transposing~~ an output of a final round from the plurality of transformation rounds.

49. (Canceled).

50. (Previously presented) A method according to Claim 48 further comprising adding code to transpose the respective round key for each of the plurality of transformation rounds.

51. (Previously presented) A method according to Claim 48 wherein each respective round key is applied according to a round key schedule.